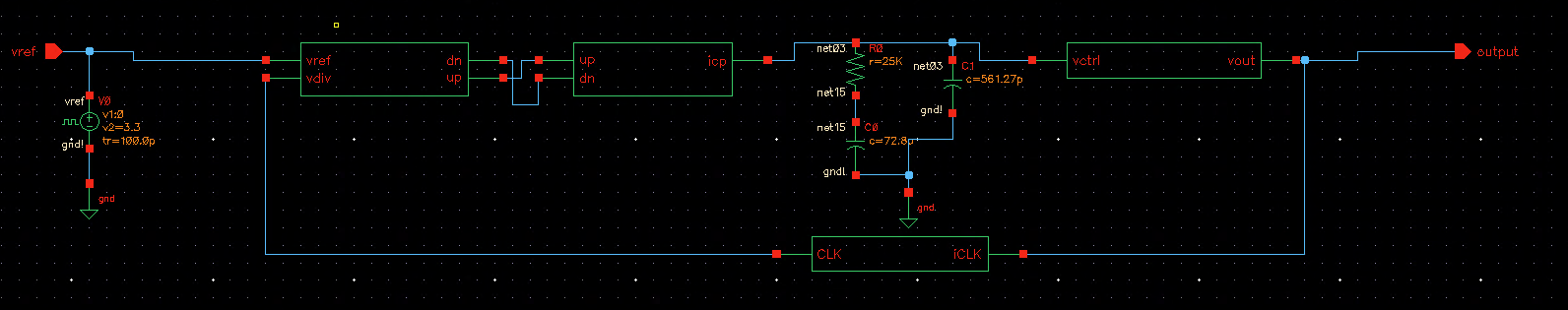
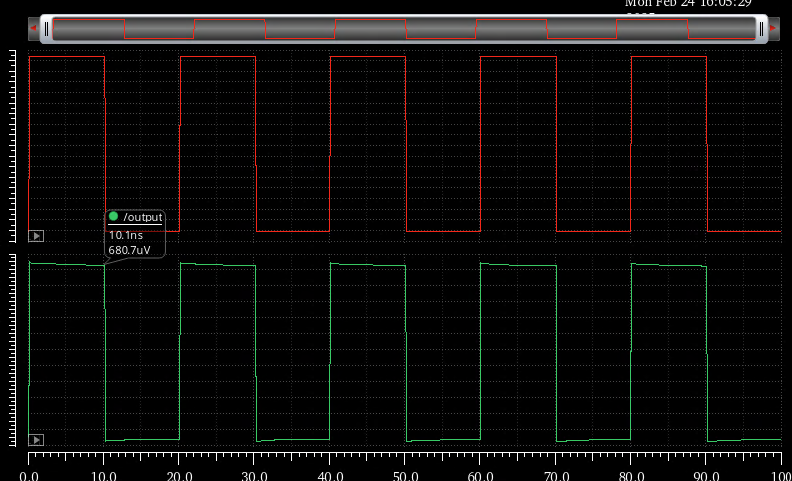
**PLL**

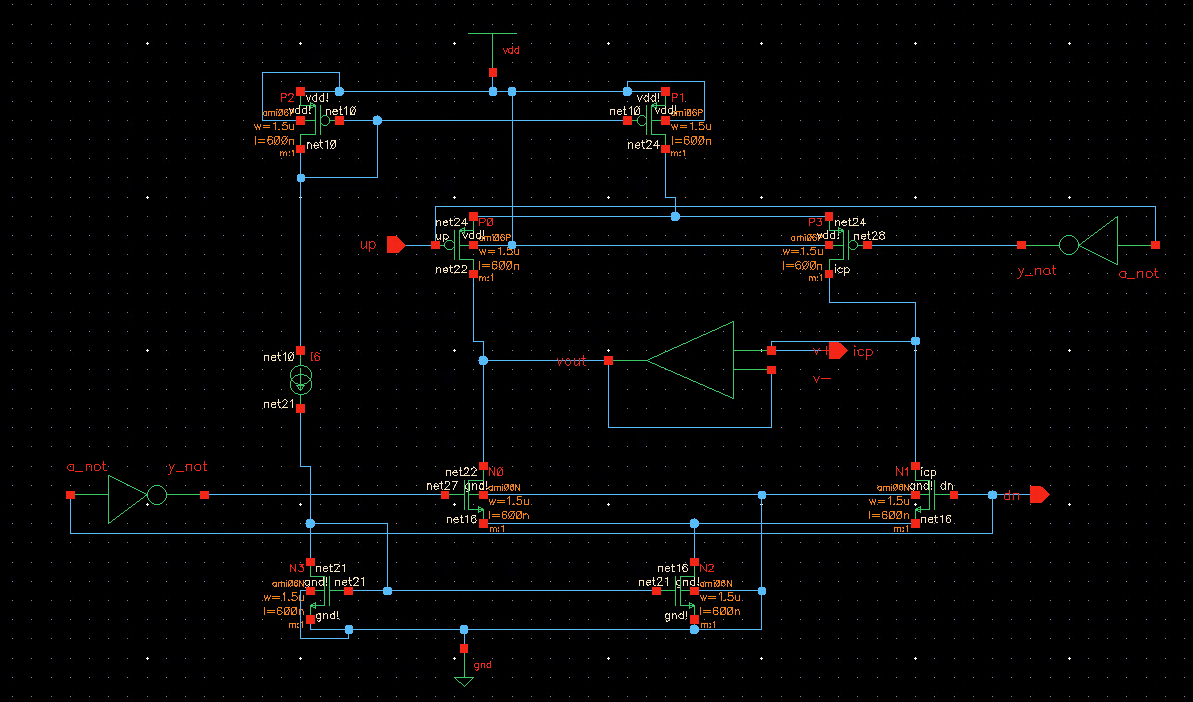
Final Diagram

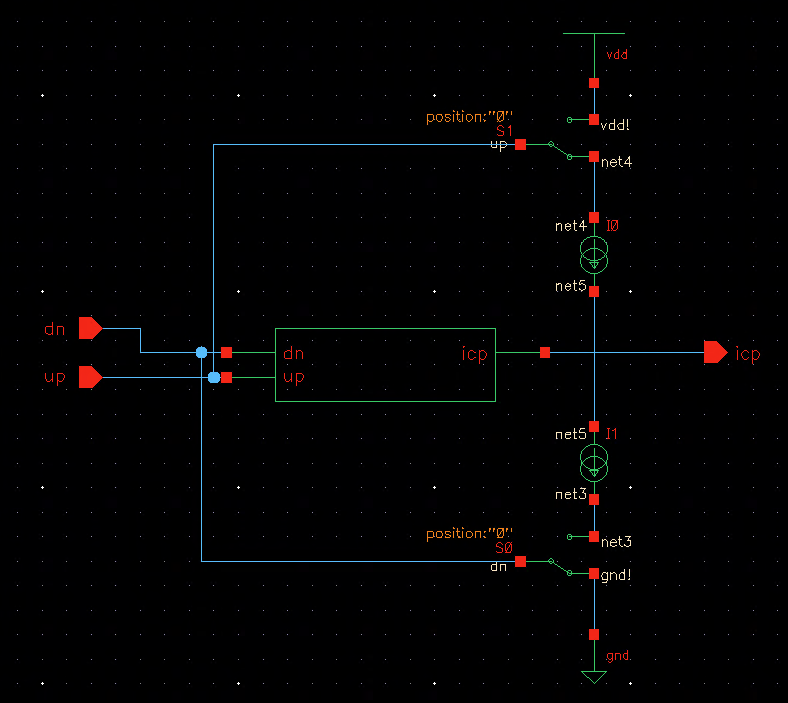


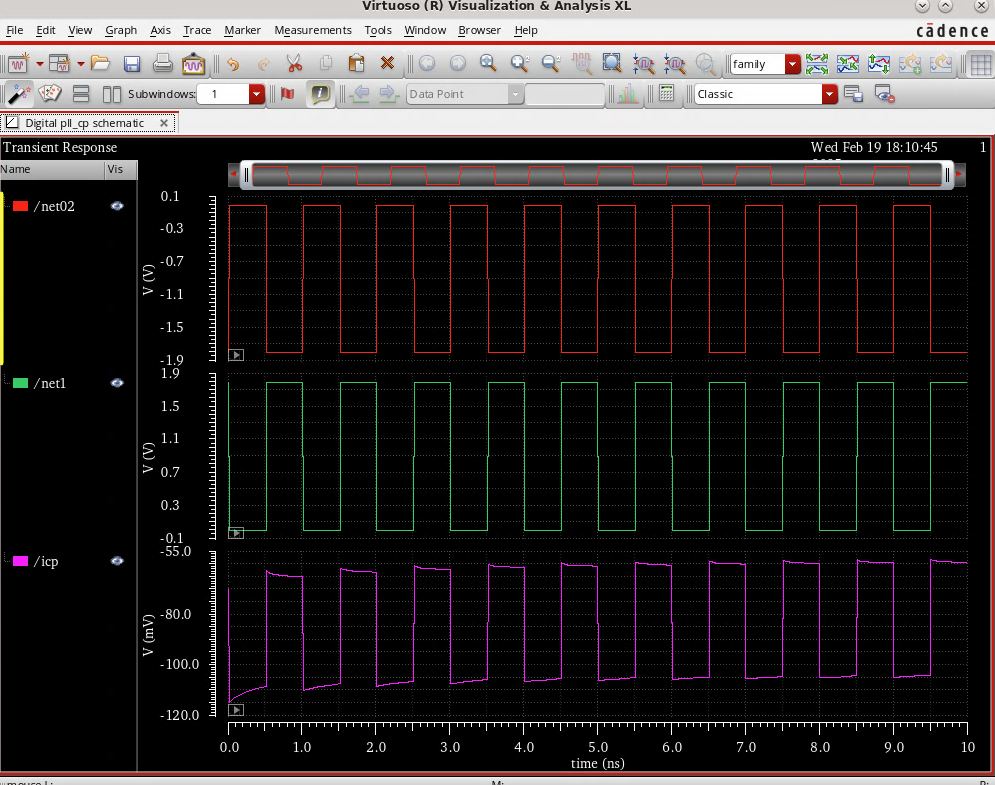
Output Waveform



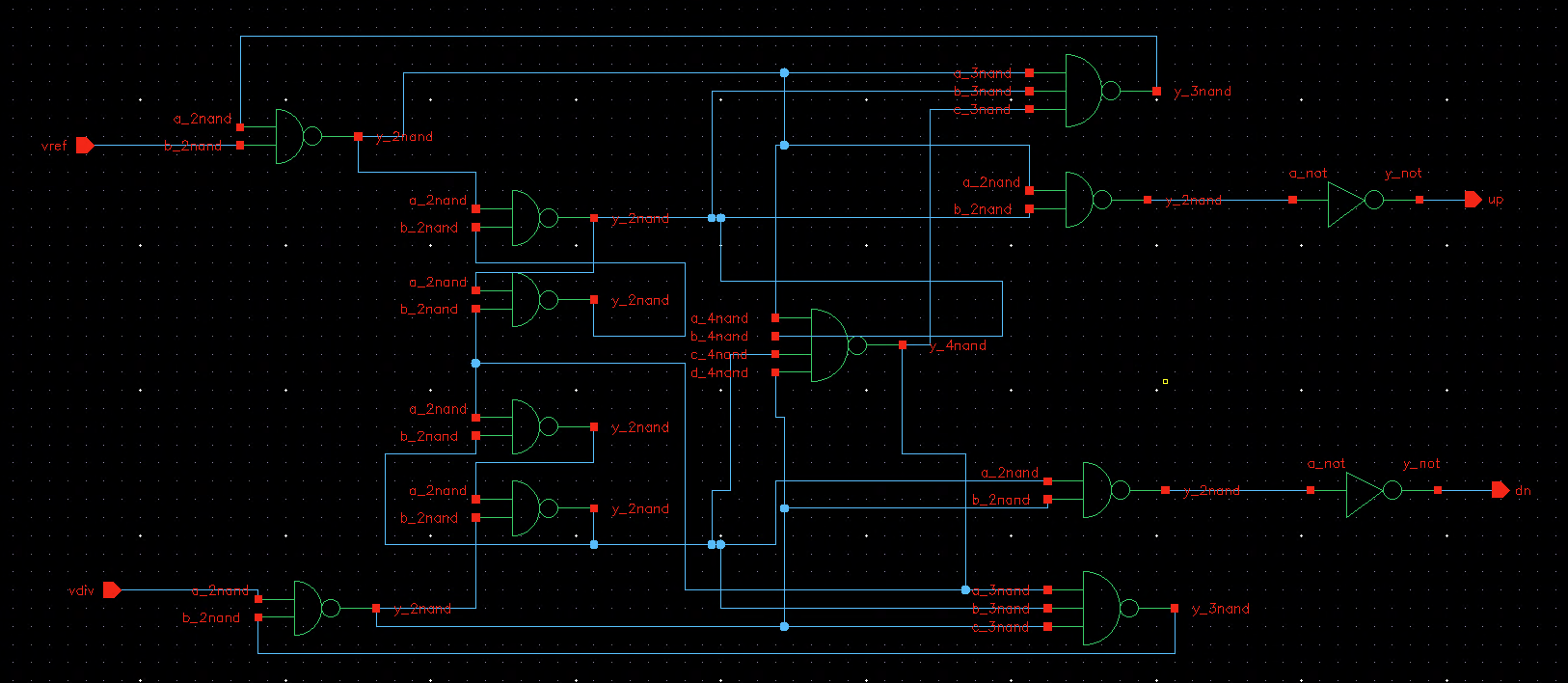
Charge Filter

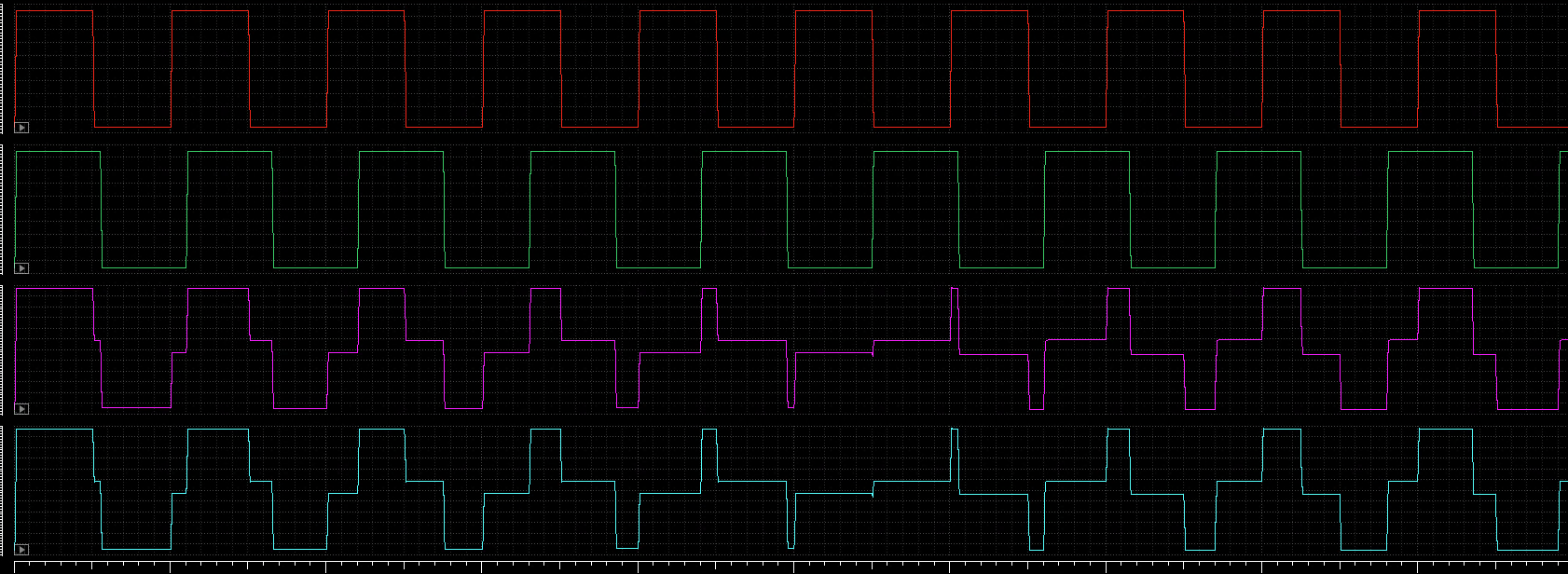




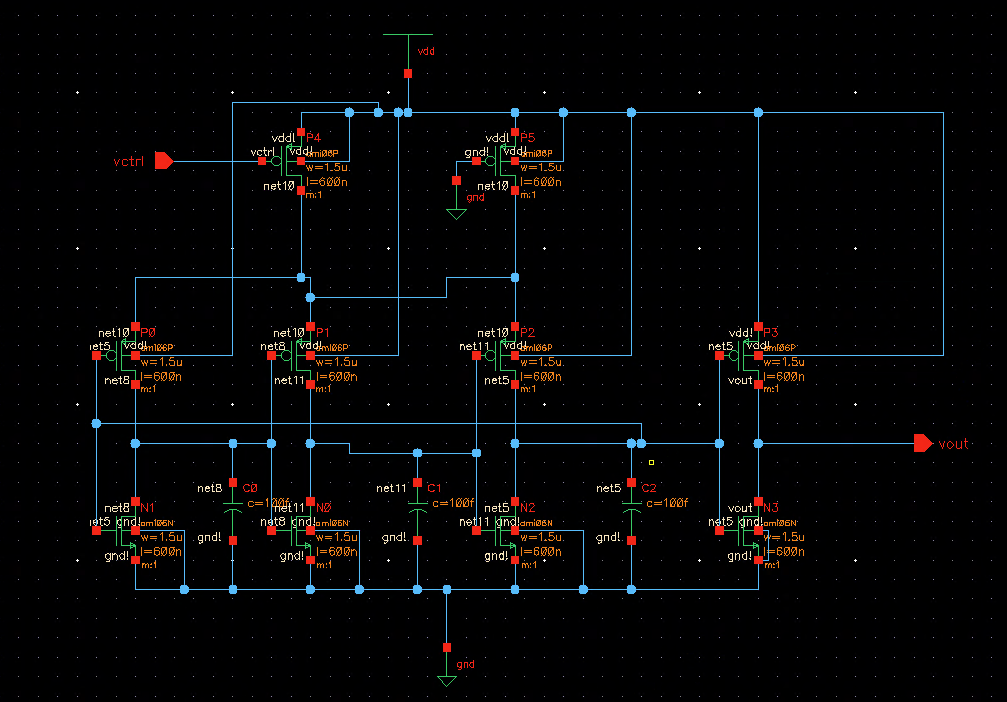


Phase-Frequency Detector





Voltage Controlled Oscillator



Divider circuit

